



# cebek®

## CHIP CARDS

### C-7294



Smart cards, which incorporate a chip of 1-KByte EEPROM with write protect function and Programmable Security Code (PSC). They are suitable for professional applications access control, scheduling, equipment, processes, pricing, security and presence in companies, institutes, schools, etc. As a complement, we have the ISO card connector, ref. C-5420, soldered to printed circuit board. Your contacts are self cleaning and mechanical life is greater than 100,000 cycles. It has a card detector that is activated after inserting it and have attached the contact.

The card contains the FM4428 EEPROM, which is fully compatible with SLE4428. It guarantees a minimum of 100,000 cycles write / erase. The data retention is at least 10 years. The contact configuration and serial interface are designed in accordance with ISO-7816 (synchronous transmission). The EEPROM is organized 1024 x 8 bit offering the possibility of programmable write protection for each byte. All memory, except for the PSC (programmable security code), can always be read. All memory can be written / erased before the security bit is active. After activating the protection bit, read only memory. The protection bit (writing) is programmable only once and can not be deleted. The chip has a logic check PSC (programmable security code.) After eight successive incorrect entries the error counter will block any subsequent attempt at PSC verification and discounted any possibility to write and erase. Only after successfully entering the 2 byte programmable Security Code (PSC) data can be changed.

#### TECHNICAL CHARACTERISTICS

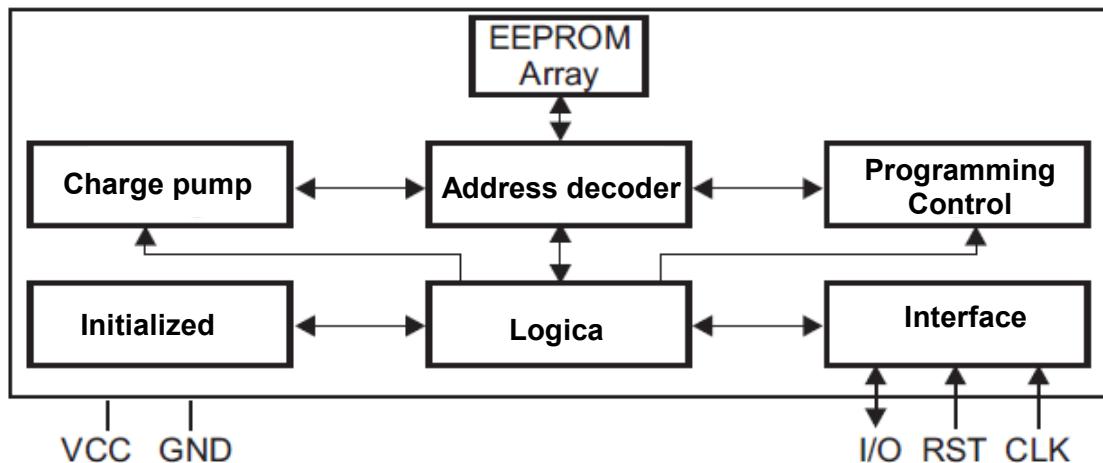
##### Absolute maximum values

		Minimum	Typical	Maximum	Units
Vcc	Alimentation	-0,3		6	V
Vi	Input voltage	-0,3		6	V
Tsto	Storage temperature	-40		125	°C
Ptot	Dispersed power			60	mW

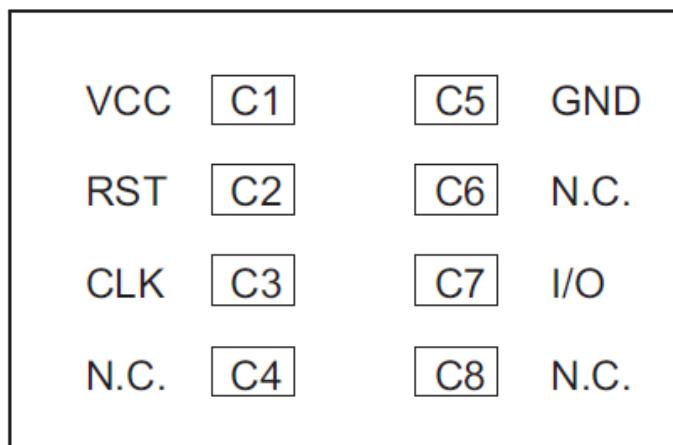
#### DC CHARACTERISTICS

		Minimum	Typical	Maximum	Units
Vcc	Voltage	4,5	5	5,5	V
Icc	Current		3	10	mA
Vih	Voltage input H (I/O, CLK, RST)	3,5		Vcc	V
Vil	Voltage input L (I/O, CLK, RST)	0		0,8	V
Ih	Current input H (I/O, CLK, RST)		3,9	10	uA
LoL	Current output L (Vi=0,4V, open drain)	0,5			mA
LoH	Current leakage H (Vi=Vcc. Open drain)			10	uA
Ci	Input Capacity			10	pF
F	Clock frequency	20			kHz
T	Test pin		Open or Vss		

## BLOCK DIAGRAM.



## CONNECTIONS



Contact	Symbol	Function
C1	VCC	Working voltage
C2	RST	Control RESET
C3	CLK	Clock
C4	N.C.	Not connecte
C5	GND	Mass
C6	N.C	Not connecte
C7	I/O	Online data (open drain)
C8	N.C.	Not connecte

